

**AMENDMENTS TO THE SPECIFICATION**

**Please add the following new paragraph after the Title on page 1:**

This application is a continuation application of U.S. Patent No. 6,671,722, entitled "Stack-less, CPU-less Creation of Valid SNMP Trap Packets", filed July 8, 1999, and issued on December 30, 2003.

**Please replace the paragraph beginning on page 6, line 5, and ending at page 6, line 12, with the following amended paragraph:**

Fig. 2 provides a block diagram of an embodiment of the invention in the form of an IC (Integrated Circuit) 10, in this case, an Application Specific Integrated Circuit (ASIC). The IC 10 comprises an event processor 20 ~~for 20~~, for receiving an indication of an event, a packet generator 22 ~~22~~, for receiving an event code and event data from the event processor, ~~processor~~ and for accessing a packet template via template storage 24 ~~24~~, to generate a packet including an SNMP Trap PDU packet ~~packet~~, based on the packet template stored in template storage 24. The packet generator 22 sends the packet through bus control 26 to a communication controller for transmission over a shared medium, such as an ethernet network.

**Please replace the paragraph beginning on page 13, line 11, and ending at page 13, line 16, with the following amended paragraph:**

Fig. 8 provides a more detailed view of the packet generator 22. Data from the event processor 20, including but not limited to an event code and event data, are sent to packet control 50 within the packet generator 22. Packet control 50 accesses template storage in order to include the event code and event data into the packet template and causes the packet template to be transmitted over the first bus 32, via the bus master 30, to the communication controller.

**Please replace the Abstract on page 23 with the following amended Abstract:**

~~A device, such as an Application Specific Integrated Circuit (ASIC) which has access to a memory, such as non-volatile RAM (NVRAM) or Electrically Erasable Programmable Read Only Memory (E<sup>2</sup>PROM). The device may reside in a PC or on a network interface card for providing an interface between the PC and a network, such as an Ethernet based network. Software on the PC constructs a base packet or template for an SNMP trap PDU and stores the template into the NVRAM or E<sup>2</sup>PROM associated with the device. When the device determines the need to generate and send the SNMP trap PDU, the device can, without a CPU and without a full implementation of network layer software stacks, generate the SNMP trap PDU based on the base packet stored in the NVRAM. The device need only insert the non-static data into the packet built from the base packet before sending the packet to a communication~~

~~controller, such as an Ethernet controller, which subsequently sends the packet over a network, such as the Ethernet based network.~~ In one embodiment, a method is provided. The method of this embodiment provides accessing a packet template in a memory, the packet template having at least one static field, and in response to an indication of an event, generating a packet on an integrated circuit, the packet being based on the packet template.